

Application No.: 10/655,845

Docket No.: JCLA10853

**AMENDMENTS****In The Claims:**

Claim 1. (currently amended) A standardized circuit board core, comprising:

a dielectric core layer having a first surface and a related second surface, and divided into a plurality of partial areas; and

a plurality of solid conductive posts passing through the dielectric core layer and connecting to the first surface and the second surface of the dielectric core layer, wherein the conductive posts are arranged in an array form ~~one form selected from a group of array form and a constant distance form~~ in at least one of the partial areas of the dielectric core layer.

Claim 2. (currently amended) The standardized circuit board core of claim 1, wherein further comprises two conductive layers being formed on the first surface and the second surface of the dielectric core layer respectively, and the conductive layers are intact and not patterned.